

- 2 -

IN THE SPECIFICATION

Please amend the paragraph spanning lines 14-28 on page 9, as follows:

Referring now to Figure 4, a detailed diagram of Marker Insertion Module 308, and Data Busses 310 and 322 are shown. The interaction of the various control busses and system parameters used during the operation of the present invention are also described. Marker Insertion Module 308 includes a Buffer 402 having a predetermined number of registers, where each register can store a single dword. In the example shown in Figure 4, Buffer 402 utilizes ten (10) registers 404- 422, each of which can store a 32-bit dword, although Buffer 402 could easily be modified to accommodate dwords of any width, or could be modified to have greater depth, for example, in the form of a register queue. A number of parameters affect the overall performance of Marker Insertion Module 308. The width of Data Bus 310 is represented by the parameter (DBin). In the example shown in Figure 4, DBin = 128 bits, or four (4) 32-bit dwords. Thus, four (4) 32-bit dwords can be read into the registers of Buffer 402 in a single clock cycle. The width of Data Bus 322 is represented by the parameter (DBout). In the example shown in Figure 4, DBout = 128 bits, or (4) 32-bit dwords. Thus, four 32-bit dwords can be read out of Buffer 402 in a single clock cycle.